

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

|   |   |                           |
|---|---|---------------------------|
| In re Patent Application of:            | ) | Atty Docket No.:          |
| <b>FONDEUR ET AL</b>                    | ) | <b>78297 (P1680 US)</b>   |
|   | ) |                           |
| Serial No. <b>10/736,295</b>            | ) | Art Unit: <b>1756</b>     |
|   | ) |                           |
| Filing Date: <b>DECEMBER 15, 2003</b>   | ) | Examiner:                 |
|   | ) | <b>STEPHEN D. ROSASCO</b> |
| Confirmation No. <b>3793</b>            | ) |                           |
|   | ) |                           |
| For: <b>PLANAR CIRCUIT OPTIMIZATION</b> | ) |                           |
|   | ) |                           |
|   | ) |                           |

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**EFILED**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Dear Sir:

In response to the Office Action of October 4, 2006,  
please amend the above-identified application as follows:

**Amendments to the Drawings** begin on page 2 of this paper and  
include an attached replacement sheet.

**Amendments to the Claims** are reflected in the listing of  
claims, which begins on page 3 of this paper.

**Remarks** begin on page 10 of this paper.

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